

**REMARKS**

By the foregoing amendments, claims 1 and 9 have been amended, and new claims 11 and 12 have been added. Thus, claims 1-12 currently are pending and are subject to examination in the above-captioned patent application. No new matter is added by the foregoing amendments, and these amendments are fully supported by the specification. Applicants respectfully request that the Examiner reconsider the above-captioned patent application in view of the foregoing amendments and the following remarks.

In the Office Action mailed April 2, 2004, the Examiner rejects claims 1-10 under 35 U.S.C. § 102(e), as allegedly being anticipated by U.S. Patent No. 6,408,100 B2 to Miyawaki et al. ("Miyawaki"). Applicants respectfully traverse this anticipation rejection, as follows:

Applicants' independent claim 1 describes an MPEG video decoder, comprising "a frame memory having a plurality of banks, wherein each of said bank stores one picture and the parameters of each layer decoded by said image decoding section by mutually relating the picture and the parameters, wherein the layer includes a sequence layer." As such, in Applicants' independent claim 1, **each of:** (1) the parameters included in the sequence layer; **and** (2) the decoded picture, **are stored in the frame memory**, which allows the present invention to correctly display the MPEG bit stream, e.g., in a slide show, even when there is no continuing pictures or sequences. Applicants' independent claim 9 includes similar method limitations corresponding to the above-described limitations of independent claim 1.

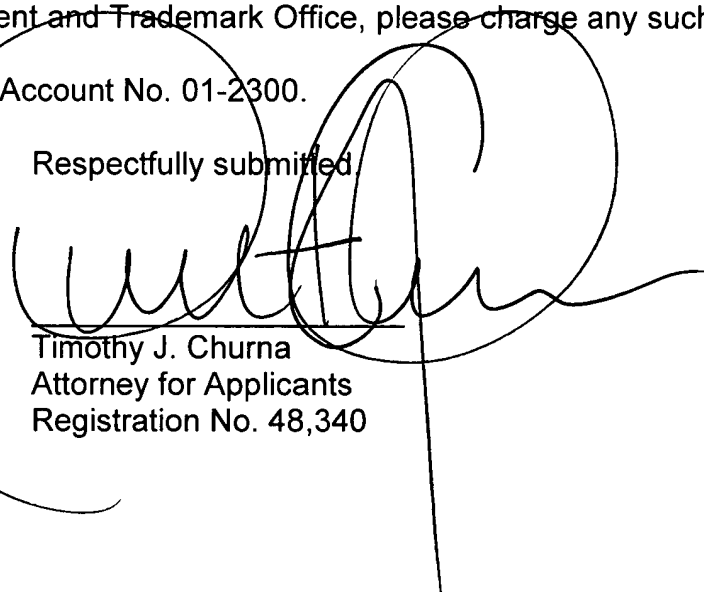
In contrast to Applicants' claimed invention, Miyawaki describes a video decoder that is similar to the video decoders described in the Description of Related Art section of the above-captioned patent application. See, e.g., Appl'n, Page 4, Lines 4-8. Specifically, Miyawaki describes a video decoder in which display parameters and a picture coding type are separated, and the separated display parameters are stored in **register groups** while the decoded picture is stored in the memory. See, e.g., Miyawaki, Column 1, Lines 60-68; Column 2, Lines 1-10; and Column 4, Lines 18-32. Because the display parameters are stored in the register groups and the decoded picture is stored in the memory, Miyawaki fails to disclose or suggest a video decoder in which **each of:** (1) the parameters included in the sequence layer; **and** (2) the decoded picture, **are stored in the frame memory**, as set forth in independent claims 1 and 9. Therefore, Applicants respectfully request that the Examiner withdraw the rejection of independent claims 1 and 9.

Claims 2-8 and 10 depend from allowable independent claims 1 and 9, respectively. Therefore, Applicants respectfully request that the Examiner withdraw the rejection of claims 2-8 and 10.

**CONCLUSION**

Applicants respectfully submit that the above-captioned patent application is in condition for allowance, and such action is earnestly solicited. If the Examiner believes that an in-person or telephonic interview with Applicants' representatives would expedite the prosecution of the above-captioned patent application, the Examiner is invited to contact the undersigned attorney of records. Applicants are filing a Petition For a Two-Month Extension of Time with this response, and are enclosing a check in the amount of \$420.00 covering the requisite large entity fee for such an extension of time. Nevertheless, in the event of any variance between the fees determined by Applicants and those determined by the U.S. Patent and Trademark Office, please charge any such variance to the undersigned's Deposit Account No. 01-2300.

Respectfully submitted



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Enclosure: Petition for Two-Month Extension of Time